

COMS Gate array/BU12100 Series

●BU12100 Series

Parameter		BU12101	BU12102	BU12103	BU12104	BU12105	BU12106	BU12107	BU12108	BU12109	BU12110	BU12111
Integrated counts	Row gates(2 input NAND)	1224	2496	3240	4352	5616	9200	14616	21280	31280	39140	66960
	Usable gates(55%)	673	1372	1782	2393	3088	5060	8038	11704	17204	21527	36828
	No. of inputs/output	32	44	48	56	72	88	108	128	152	168	216
Function	Supply voltage range	5.0±10%										
	Delay time	0.9nsec(5.0V,FO=2,AL=2mm)										
	Input/output level	CMOS、TTL										
	Toggle frequency	250MHz										
Features		High current output 48mA(Triple buffer) Internal RAM, possible Low power library										

●Package

	VDD,GND pin		BU12101	BU12102	BU12103	BU12104	BU12105	BU12106	BU12107	BU12108	BU12109	BU12110	BU12111
	GND	VDD											
SDIP30	15	30	●	●	●	●	●	●					
SDIP32	16	32	●	●	●	●	●	●					
DIP40				●	●	●	●						
SOP20	10	20	●										
SSOP-A20	10	20	●	●	●								
SSOP-A24	12	24	●	●	●								
QFP32	12	28	●	●	●	●	●						
QFP44	17	39		●	●	●	●	●	●	●			
VQFP48	18	42			●	●	●	●		●			
SQFP56	22	49				●	●	●	●	●			
QFP64	24	56					●	●	●	●			
VQFP64	24	56						●	●	●	●	●	
QFP80	3,22,33,49,62	2,23,42,63						●	●	●			
SQFP80	1,20,30,41,60	21,40,61,80						●	●	●	●	●	●
VQFP80	1,20,30,41,60	21,40,61,80						●	●	●			
SQFP100	1,28,40,53,80	2,29,52,79							●	●	●	●	●
VQFP100	Under development	Under development							○	○	○	○	
QFP120	15,31,60,75,91,120	1,30,61,90								●	●	●	●
SQFP144	19,37,55,72,91,109,127,144	1,36,73,108									●	●	●
SQFP160	21,41,42,79,80,101,121,122,159,160	1,20,40,81,100,120										●	●

●Mass-production is possible : ○Under Development : △Under planning

Parenthesized number is the max. number of signal pins. Non-connection pins are identified on request.

Notes : About GND and V_{DD}

1.For multi pin type packages GND and V_{DD} pins may be added to improve operation by reducing effects of noise.(GND and V_{DD} pins are selectable)

2.If simultaneous changes occur on the bus line frequently, we can present the best pin assignment after consultation.